

The EPEPS 2023 call for papers is out! The 32nd IEEE Conference on Electrical Performance of Electronic Packaging and Systems will be held on October 15-18 2023 in Milpitas, CA.

Milpitas will welcome attendees in the heart of the Silicon Valley, within short distance of many prominent microelectronic companies, and of the San Jose and San Francisco international airports.

You are invited to submit papers in PDF format, up to 3 pages long. Submit papers online by **July 17, 2023**, at:

<http://www.epeps.org/submission.html>

Accepted papers will be published on IEEEExplore. Selected papers will be invited for a special issue in IEEE Transactions on Components, Packaging, and Manufacturing Technology. Full details can be found on the Call for Papers:

<http://www.epeps.org/papers.html>

Some **highlights** about the conference:

- **Top-Notch 4-day Program**, with keynotes, presentations, posters and demo sessions focused on the latest advancements in the field from academia and industry;

- **Four Prestigious Awards** for the best contributions: "Best Paper Award", "Best Poster Paper Award", "Best Student Paper Award", "Best Benchmark Paper Award"

<http://www.epeps.org/awards.html>

- **Tutorials**: a full day of tutorials on the latest advancements in package/PCB design, SI and PI modeling, high-speed SerDes simulation, high precision measurement techniques and novel interconnect designs;

- **IEEE Education Credits**: attend EPEPS and earn professional development hours (PDHs) and continuing education units (CEUs);

- **Student Travel Grants**, offered by the IEEE EPS Society

<http://www.epeps.org/awards.html>

- **Exhibit & sponsorship**: demonstrate your latest products, components, and tools to a specialized audience from both industry and academia. Further enhance your company visibility with sponsorship opportunities!

<http://www.epeps.org/exhibits.html>

Conference Co-chairs:

Swagato Chakraborty, Siemens EDA (Swagato.Chakraborty@siemens.com)

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