

Expanding Packaging Technologies into Antenna Technology

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Since the mid-1990s, complementary metal oxide semiconductor has been the technology driver for the wireless revolution. It is in fact now the dominant technology for the fifth generation (5G) New Radio (NR).

AiP technology integrates an antenna (or antennas) with a radio chip (or chips) in a package [1, 2]. It balances performance, size, and cost well. Hence, it has been the antenna and packaging technology for millimetre-wave (mmWave) 5G NR.

AiP technology has fundamentally changed the ecosystem of radios and radars for wireless applications: (1) It has enhanced the operation of the multi-scale multi-physics co-design platform. (2) It has promoted the development of new materials and processes for advanced packaging. Currently, flip-chip AiP dominates in mmWave 5G market. However, fan-out AiP with much thinner die to die integration is promising for more compact mmWave 5G NR integration. (3) It has advanced the measurement of antennas and packages to an unprecedented level. New testing strategies and equipment have been specifically developed for fast functional verification of AiP in production lines. (4) It has enriched the techniques to suppress electromagnetic interference. The conformal shield and compartment shield using laser trench, paste filling, and metal coating are effective to suppress electromagnetic interference in AiP. (5) It has found many other applications, including Internet of Things devices at 2.4 GHz, virtual reality, augmented reality, and gesture radars at 60 GHz, automotive radars at 79 GHz, imagers at 94 GHz, sensors at 122, 145, and 160 GHz, as well as 300-GHz wireless links. The advantages of AiP technology will continue to generate new applications, for example, the use of AiP technology in mmWave 5G based industrial Internet has huge potential.

Recently there have been many reports of AiP element designs and research using different fabrication processes. But in order to cover the whole 5G NR band from 24.25 to 40 GHz, a broadband and miniaturized AiP design is necessary to reduce the set of arrays and achieve

better performance. Such design has been reported by Lin et al. in 2020 [3] (Fig. 1), which used Yagi elements instead of dipole-type radiators to reduce the total size and successfully achieved excellent endfire radiation capability. Glass packaging technology was utilized because it can support low-cost and scalable structures with better surface roughness and coefficient of thermal expansion match to silicon dies.

Additive manufacturing techniques, such as inkjet printing and 3D printing, have become especially popular for AiP because they are customized, low-cost, user-friendly and cause less waste of materials [4]. Inkjet printing technology enables the deposition of a wide variety of materials such as dielectrics, conductors, and semi-conductors onto rigid or flexible substrates. Inkjet-printed interconnects feature a more rugged, planar, and conformal structure, which offers an improved RF performance even in challenging configurations [5]. Fully inkjet printed sloped “ramp” interconnects (Fig. 2) on 3D printed substrates can form smoother transitions from a molded IC die to an external plane of its encapsulation with lower loss up to 95GHz [6, 7]. 3D printing technologies like FDM and SLA printing demonstrate significant advantages in creating selective customized vertical structures for more compact and complex multilayer integration. The combination of inkjet printing and 3D printing makes it possible to implement innovative 3D packaging design [8]. It is also possible to integrate reconfigurable “origami-inspired” structures into RF designs (Fig. 3) to enable more tunability for more application scenarios [9, 10].

In summary, AiP technology impacts on both antenna and packaging industries tremendously. The traditional antenna industry has begun to lose some business, while the packaging companies has expanded their business into the antenna domain for the first time [11].

Figures

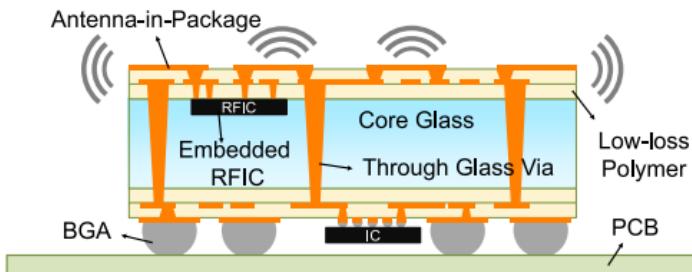


Fig. 1 Three-dimensional SiP module with AiP designs for 5G applications [3].

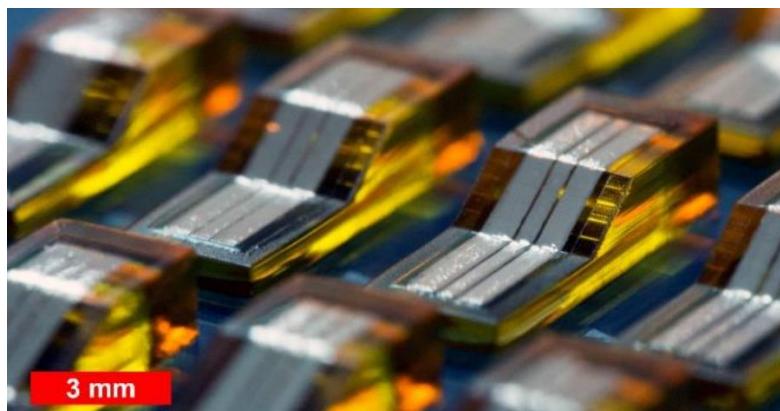


Fig. 2 3D-printed ramp structures with inkjet-printed CPW interconnects, highlighting the 35° slope ramps [7].

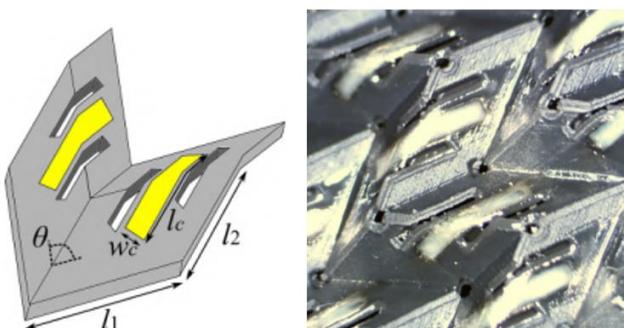


Fig. 3 3D-printed and inkjet printed origami-inspired reconfigurable frequency selective surface element [10].

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