

## 2016 SCHEDULE OF HIR WORKSHOPS

1. [ECTC + IThERM](#) Las Vegas, NV USA 05/31- 06/04
2. Palo Alto Workshop before SEMICON WEST (3101 Alexis Drive, Palo Alto. CA ) 07/10
3. [SEMICON WEST](#) San Francisco, CA USA 07/11
4. Nagase R&D Center Tokyo, Japan 8/9
5. ITRI Taiwan 8/12
6. [ICEPT](#) Wuhan, China 8/16
7. [ESTC](#) Grenoble, France 09/13-16
8. IEMT-EMAP Penang, Malaysia 9/20
9. [Electronics Packaging Symposium](#) Binghamton, NY USA 10/5-8
10. [IMPACT](#) Taipei, Taiwan 10/26-28
11. [ICSJ](#) Kyoto, Japan 11/7-9
12. [MEPTEC Heterogeneous Integration Roadmap Symposium](#) San Jose CA 11/14
13. [EPTC](#) Singapore 11/30-12/03
14. [IEDM](#) San Francisco CA 12/3-7
15. Asia Workshops
  1. ITRI, Taiwan 8/12
  2. ICEPT in Wuhan, China 8/16-19
  3. Japan, Tokyo 8/9