**Date:** June 30, 2023 **Time:** 12:00 PM ET

Presenter: Giovanni A. Salvatore

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## Advanced packaging for wide bandgap semiconductor power modules

## Abstract:

WBG semiconductors (i.e. SiC and GaN) outperform silicon in terms of breakdown field (x10 in case of SiC) and thermal conductivity (x2 in case of SiC), hence, ensuring smaller conduction losses and better heat dissipation combined with faster switching frequency. All this results in saving energy (i.e. smaller battery) and costs (i.e. smaller passive components and volume of the power module). However, to take fully advantage of WBG semiconductors it is of paramount importance to: (i) develop new packaging technologies, (ii) optimize the packaging design in terms of thermo-electric and electromagnetic (i.e. stray inductance and mutual coupling) behavior to ensure clean and fast switching of the MOSFETs and avoid oscillations. This talk will start from basic concepts of power module design by taking a half-bridge topology as reference and will show some of the tools and methodologies that are currently used for the optimization. Moreover, the talk will briefly describe some of the new advanced technologies (embedding, chip scale packaging, sintering) for the packaging of GaN and SiC power module.

## Bio:



Dr. Giovanni A. Salvatore is an Assistant Professor at University of Venice Ca Foscari since July 2021. He got his bachelor in Electronics and Master in Micro&Nanotechnology from the Polytechnic of Turin in 2004 and 2006, respectively. He received the PhD in 2011 from EPFL for his research on ferroelectric transistors for memory and switch application. His doctoral dissertation was awarded the 2012 ABB Award. From 2011 to 2017, he has worked at ETH Zurich and at UIUC (Rogers' group) as senior researcher and group leader with focus on thin film electronics, wireless epidermal devices and dissolvable electronic components. At the end of 2017, he joined the ABB Corporate Research Center in Baden (CH) to work on packaging and reliability of electric devices. He is author of more than 100 peer-reviewed publications and about 10 patents. His current interests fall into the concept of "sustainable electronics" with examples in biodegradable materials and

devices and in efficient power converters.

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